Serial No. 10/603,893 Invention: Switched	Ruotsalainen et al. Filing Date 6/24/2003 Voltage-Mode Dither	Confirmation No. 4891 Signal Generation For A S	Examiner Young, Brian K.	Art Unit
10/603,893	6/24/2003	4891		
			Young, Brian K.	2010
Invention: Switched	Voltage-Mode Dither	Signal Generation For A S		2819
		Address to: Commissioner for Paten P.O. Box 1450		
ransmitted herewith	are:	Alexandria, VA 22313-14	50	
3 sheets	of formal drawing(s)	for this application.		
☐ · Each sheet of d	rawing indicates the id	dentifying indicia suggeste	ed in 37 CFR Section 1.	.84(c).
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		2.	Dated: N	Лау 20, 2004

Customer No. 29683

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I certify that this document and attached formal drawings are being deposited on May 20, 2004 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature of Person Mailing Correspondence

Elaine F. Mian

Typed or Printed Name of Person Mailing Correspondence